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PATENT 5500-88700/TT5374

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Filed: Invento	ation No.: 10/726,902 December 3, 2003 ors: chell Alsup and Gregory	\$ \$ \$ \$	Examiner: Group/Art Unit: Atty. Dkt. No:	Fennema, Robert E 2183 5500-88700
	liam Smaus Transitioning From Instruction Cache to Trace Cache on Label Boundaries	,	I hereby certify that this continued States Postal States Postal States Postal States Postal States mail in an envelor Patents, P.O. Box 1450, date indicated below.	orrespondence is being deposited with Service with sufficient postage as first upe addressed to Commissioner for Alexandria, VA 22313-1450, on the ert C. Kowert Printed Name May 16 2006 Date
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INFORMATION DISCLOSURE STATEMENT

P.O. Bo	ox 1450	for Patents . 22313-1450
Sir:		
	Applica	ant requests consideration of \(\sum \) the references listed on the attached Form PTO-
1449 ar	nd/or 🛚	the additional information identified below in paragraph 3.
	\boxtimes	A copy of each required reference listed on the Form PTO-1449 is enclosed.
		The references listed on the Form PTO-1449 were previously cited in application serial no to which the captioned application is seeking priority under 35 U.S.C. §120; therefore, a copy of each reference is not enclosed.
1.	This In	formation Disclosure Statement is submitted:
	a.	within 3 months of the filing date of a national application other than a
		continued prosecution application under § 1.53(d); within 3 months of the date of entry of the national stage as set forth in §
		1.491 in an International application;

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before the mailing date of a first Office Action on the merits; or

	b.	before the mailing of a first Office Action after the filing of a request for continued examination under § 1.114. In after the events of above paragraph 1a and prior to the mailing date of a final Office Action or Notice of Allowance, and thus: ☐ the certification of paragraph 2 below is provided, or ☐ a fee of \$180.00 (authorized below).
	c.	after the mailing date of a final Office Action or a Notice of Allowance and prior to payment of the issue fee, and thus: the certification of paragraph 2 below is provided and a fee of \$180.00 is enclosed.
2.	It is he	reby certified:
		that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three (3) months prior to the filing of the Statement; or
·		that no item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the person signing the certification after making reasonable inquiry, was known to any individual designated in § 1.56 (c) more than three (3) months prior to the filing of the Statement.
		that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application and that this communication was not received by any individual designated in § 1.56(c) more than thirty (30) days prior to the filing of the information disclosure statement;
		 U.S. Application Serial No. 10/615,506 (5500-79500) U.S. Application Serial No. 10/700,033 (5500-91600) U.S. Application Serial No. 10/755,742 (5500-90300) U.S. Application Serial No. 10/676,437 (5500-88800)
3.	\boxtimes	Consideration of the following additional information (including any co-pending or abandoned U.S. applications, prior uses and/or sales, etc.) is requested:
4.	For eac	ch non-English language reference listed on the attached Form PTO-1449:
		reference is made to an English language translation submitted herewith, and/or
		reference is made to a foreign patent office search report (in the English language) submitted herewith, and/or
		reference is made to an English language translation of a foreign patent office search report submitted herewith, and/or
		reference is made to the concise explanation contained in the specification of the present application at page(s), and/or

Application Serial No. 10/726,902 - Filed December 3, 2003

		reference is made to the concise explanation set forth below:
5.		Applicant also offers the following comments for the Examiner's consideration:
6.		Also enclosed is a copy of a foreign search report citing these references.
7.		The listed documents were brought to the attention of the Applicant(s) after payment of the issue fee in the captioned case. The documents were cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. Applicant(s) request this Information Disclosure Statement and attached Form PTO-1449 be placed in the file of the captioned application.
8.		Applicant(s) requests that the Information Disclosure Statement and attached Form PTO-1449 and references, which are being filed before the grant of the patent and pursuant to 37 C.F.R. § 1.97(i), be placed in the file of the captioned application.
	If any	required fees are missing, the Commissioner is authorized to charge said fees to
Meyer	tons, H	ood, Kivlin, Kowert & Goetzel, P.C. Deposit Account No. 501505/5500-
88700/	RCK.	

Respectfully submitted,

Robert C. Kowert Reg. No. 39,255

Attorney for Applicant(s)

MEYERTONS, HOOD, KIVLIN, KOWERT & GOETZEL, P.C. P. O. Box 398 Austin, Texas 78767 (512) 853-8800

Date: May 16, 2006

PTO/SB/08A (07-05)
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Substitute for form 1449/PTO Modified	Complete	Complete if Known		
	Application Number	10/726,902		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary) Sheet 1 of 2	Confirmation No.: Filing Date: First Named Inventor: Art Unit: Examiner Name: Attorney Docket Number:	4177 December 3, 2003 Mitchell Alsup 2183 Fennema, Robert E. 5500-88700		

			U. S. PATE	NT DOCUMENTS	
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code (if known)	Publication Date MM- DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US-7,003,629	2/21/06	Alsup	
				,	
		US-6,449,714	9/10/02	Sinharoy	
		US-6,339,822	1/15/02	Miller	
		US-6,256,727	7/3/01	McDonald	
		US-6,185,675	2/6/01	Kranich, et al.	
		US-6,345,295	2/02	Beardsley, et al.	
		US-5,930,497	7/99	Cherian, et al.	
		US-6,578,128	6/03	Arsenault, et al.	1.2
		US-6,973,543	12/05	Hughes	
		US-6,823,428	11/23/04	Rodriguez, et al.	
		US-6,167,536	12/00	Moann	
		US-6,357,016	3/02	Rodgers, et al.	
		US-2002/0144101	10/02	Wang, et al.	
		US-2003/0023835	1/03	Kalafatis, et al.	
		US-2004/0083352	4/04	Lee	
		US-2004/0193857	9/04	Miller, et al.	
		US-2005/0125632	6/05	Alsup, et al.	
		US-			

	FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of	Pages, Columns, Lines, Where Relevant Passages	Check if English
initiais*	No.'	Country Code-Number-Kind Code (if known)		Cited Document	Or Relevant Figures Appear	Translation is attached
		1				
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Examiner	·	Date Considered	
Signature			

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application. Applicant's unique citation designation number (optional).

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Sheet 2	of	2	Attorney Docket Number:	5500-88700		
		NON PATENT LITER	RATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, (page(s), volume-issue number(s), publisher, city and/or country where published.			Check if English Translation is attached	
	Al	Yuan Chou, et al., "Instruction Path	h Coprocessors," March 2000	, pp. 1-24.		
	Á2	Friendly, et al., "Putting the Fill Unit to Work: Dynamic Organizations for Trace Cache Microprocessors," Dept. of Electrical Engineering and Computer Sciences, The Univ. of Michigan, December 1998, 9 pages.				
	A3	Bryan Black, et al., "Turboscalar: A High Frequency High IPC Microarchitecture," Dept. of Electrical and Computer Engineering, Carnegie Mellon Univ., June 2000, pp. 1-				
	A4	Merten, et al., "An Architectural Framework for Run-Time Optimization," June 2001, pp. 1-43.				
	A5	Jourdan, et al., "Increasing the Instruction-Level Parallelism through Data-Flow Manipulation," Intel, 11 pages.				
	A6	Hinton, G., et al., "A 0.18-MUM CMOS IA-32 Processor with a 4-GHZ Integer Execution Unit," IEEE Journal of Solid-State Circuits, Vol. 36, No. 11, November 2001, pages. 1617-1627.				
	A7	Sanjay J. Patel, et al., "replay: A H Optimization," IEEE, Vol. 50, No.	6, June 2001, pp. 590-608.			
	A8	Jacobson, et al., "Instruction Pre-Page January 1999, 6 pages.				
	A9	Bryan Black, et al., "The Block-Ba				
	A10	Palmer, et al., "Fido: A Cache That Learns to Fetch," Proceedings of the 17 th International Conference on Very Large Data Bases, Barcelona, September 1991, pp. 255-264.				
	A11	Chen et al., "Eviction Based Cache 2003 Annual Technical Conference		es," USENIX		
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Examiner		Date Considered	
Signature	•		

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